



SHEET 1 OF 1

Form PTO 1449  
(Modified)U.S. DEPARTMENT OF COMMERCE  
PATENT AND TRADEMARK OFFICEATTY DOCKET NO.  
240063US2SERIAL NO.  
10/615,920

## LIST OF REFERENCES CITED BY APPLICANT

APPLICANT  
Keiji HOSOTANI, et al.FILING DATE  
July 10, 2003

GROUP

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
	AL						
	AM						
	AN						

## FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION	
					YES	NO
	AO					
	AP					
	AQ					
	AR					
	AS					
	AT					
	AU					
	AV					

## OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

<i>Oh</i>	AW	R. SCHEUERLEIN, et al., ISSCC 2000 / Session 7 / TD: Emerging Memory & Device Technologies / Paper TA 72, pages 94-95, "TA 7.2: A 10NS READ AND WRITE NON-VOLATILE MEMORY ARRAY USING A MAGNETIC TUNNEL JUNCTION AND FET SWITCH IN EACH CELL", 2000
	AX	
	AY	
	AZ	

☐ Additional References sheet(s) attached

Examiner

*Andrew Murray*

Date Considered

*2/6/04*

\*Examiner: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.